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Notice of Allowability	Application No.	Applicant(s)	
	10/602,065	UDREA, FLORIN	
	Examiner Hoai v Pham	Art Unit 2814	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 06/24/2003.
2. The allowed claim(s) is/are 1-44.
3. The drawings filed on 06/24/2003 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 9/22/2003
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.



Hoai Pham
Patent Examiner

DETAILED ACTION

Allowable Subject Matter

1. Claims 1-44 are allowed.
2. The following is an examiner's statement of reasons for allowance: the prior of record fails to disclose the combination of a semiconductor device structure recited in the base claim(s), including the combination of the structure comprising: the third and fourth (drift) regions being so arranged that when a reverse voltage bias is applied across the first and second regions of the semiconductor layer, the third region has locally in the proximity of the first region an excess of impurity charge relative to the fourth region, and the fourth region has locally in the proximity of the second region an excess of impurity charge relative to the third region, and the total volume charge in the third region is substantially equal to the total volume charge in the fourth region (claim 1); the third and fourth (drift) regions being so arranged that when a reverse voltage bias is applied across the first and second regions of the semiconductor layer, the impurity charge in the third region varies with a positive slope along the device from the first region to the second region and the charge in the fourth region varies with a negative slope along the device from the first region to the second region and such that the total charge in the volume of the third region is substantially equal to the total charge in the volume of the fourth region (claim 2).

Art Unit: 2814

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hoai v Pham whose telephone number is 571-272-1715.

The examiner can normally be reached on M-F.

4. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael M Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

5. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Hoai Pham
Patent Examiner